

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3	("5942310" "6133967").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/30 21:05
L3	0	pixel with thin with film with transistor with column with capacitor with LDD with gate with insulating.clms.	US-PGPUB	OR	ON	2007/09/30 21:10
L4	0	257/59 and "532".ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/30 21:10
L5	2783	257/59.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/30 21:10
L6	3599	257/347.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/30 21:10
L7	1328	257/350.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/30 21:10
L8	876	257/351.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/30 21:10
L9	562	257/357.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/30 21:10
L10	1520	257/408.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/30 21:10
L11	1608	257/431.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/30 21:10

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L12	2248	257/532.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/30 21:10
L13	12857	L4 or L5 or L6 or L7 or L8 or L9 or L10 or L11 or L12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/30 21:11
S1	50	(((((LDD with gate with (overlying or overlapping))) and pixel) and TFT) and (driver adj circuit)) and (second adj gate adj electrode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/04 13:20
S2	39	S1 and (gate with second with layer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/04 13:39
S3	2	S2 and (column with spacer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/04 13:36
S4	2	S1 and (column with spacer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 17:20
S5	66	column with spacer with pixel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/04 13:38
S6	22	S5 and (n-channel or p-channel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/04 13:39
S7	16	S6 and (gate with second with layer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/04 13:42
S8	27	S5 and (gate with second with layer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/04 13:42

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S9	5	"6639265"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/18 11:53
S10	63	(((((LDD with gate with (overlying or overlapping)) and pixel) and TFT) and (driver adj circuit)) and (second adj gate adj electrode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 17:20
S11	2	S10 and (column with spacer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 17:22
S12	25	(column with shape with spacer with pixel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 17:32
S13	22	S12 and capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 17:33
S14	20	S13 and (n adj channel) and (p adj channel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/02 17:34
S15	5	"6639265"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/06 12:00
S16	1222	(n adj channel) and (p adj channel) and (driver adj circuit)and LDD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/06 12:03
S17	1	S16 and (LDD with graded)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:08
S18	357	(pixel adj (TFT or (thin adj film adj transistor))) with LDD	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/06 13:07

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S19	231	S18 and (storage adj capacitor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/06 13:07
S20	276	(LDD with graded)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:09
S21	163	S20 and (n adj channel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:31
S22	0	S21 and (decreasing with toward with channel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:10
S23	8	S21 and (decreasing with channel)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:10
S24	821	257/303.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:40
S25	23	(thin adj film) and CMOS and (graded with LDD)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:43
S26	2	S25 and driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:42
S27	1127	(thin adj film) and CMOS and (LDD) and (driver adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/22 09:44
S28	2197	LDD and pixel and capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/18 11:54

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S29	8	LDD and pixel and (storage with capacitor) and (gate with insulator with thickness with capacitor).	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/18 11:59
S30	28	LDD and pixel and (storage with capacitor) and (gate with (insulator or dielectric) with thickness with capacitor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/18 11:59
S31	37	("20040207789" "20040218112" "20050110016" "5247190" "5399502" "5643826" "5671027" "5923962" "5978061" "6088258" "6236445" "6275061" "6278131" "6285042" "6335541" "6348368" "6638781" "6639265" "6743650"). PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 18:57
S32	1375033	257/59, "347", "350", "351", "357", "408", "431", "532".ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 19:25
S33	0	257/59 and "532".ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 19:22
S34	2354	257/59.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 19:25
S35	3139	257/347.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 19:25
S36	1230	257/350.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 19:25
S37	780	257/351.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 19:25

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S38	519	257/357.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 19:25
S39	1415	257/408.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 19:26
S40	1489	257/431.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 19:26
S41	2041	257/532.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 19:26
S42	0	S33 and S34 and S35 and S36 and S37 and S38 and S39 and S40 and S41	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 19:27
S43	11437	S33 or S34 or S35 or S36 or S37 or S38 or S39 or S40 or S41	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/30 21:10
S44	2583	S43 and pixel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 19:30
S45	2421	S44 and transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 19:31
S46	2218	S44 and (thin with film with transistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/16 19:33
S47	0	pixel with thin with film with transistor with column with capacitor with LDD with gate with insulating.clms.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/30 21:09

EAST Search History

S48	19	"6465268"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/23 10:24
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